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VIAP0020USA



METHOD AND RELATED CIRCUIT FOR CLOCK GENERATION AND RECOVERY

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Attached Files:

declaration	VIAP0020DEC1.TIF
declaration	VIAP0020DEC2.TIF
specification	VIAP0020.xml
bibd-transmittal	VIAP0020apds.xml
patent-assignments	VIAP0020asgn.xml
fee-transmittal	VIAP0020fee.xml

Attached Image File(s):

VIAP0020DEC1.TIF
VIAP0020DEC2.TIF

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